

FIG. 1 is a block diagram of a prior art receiver system.

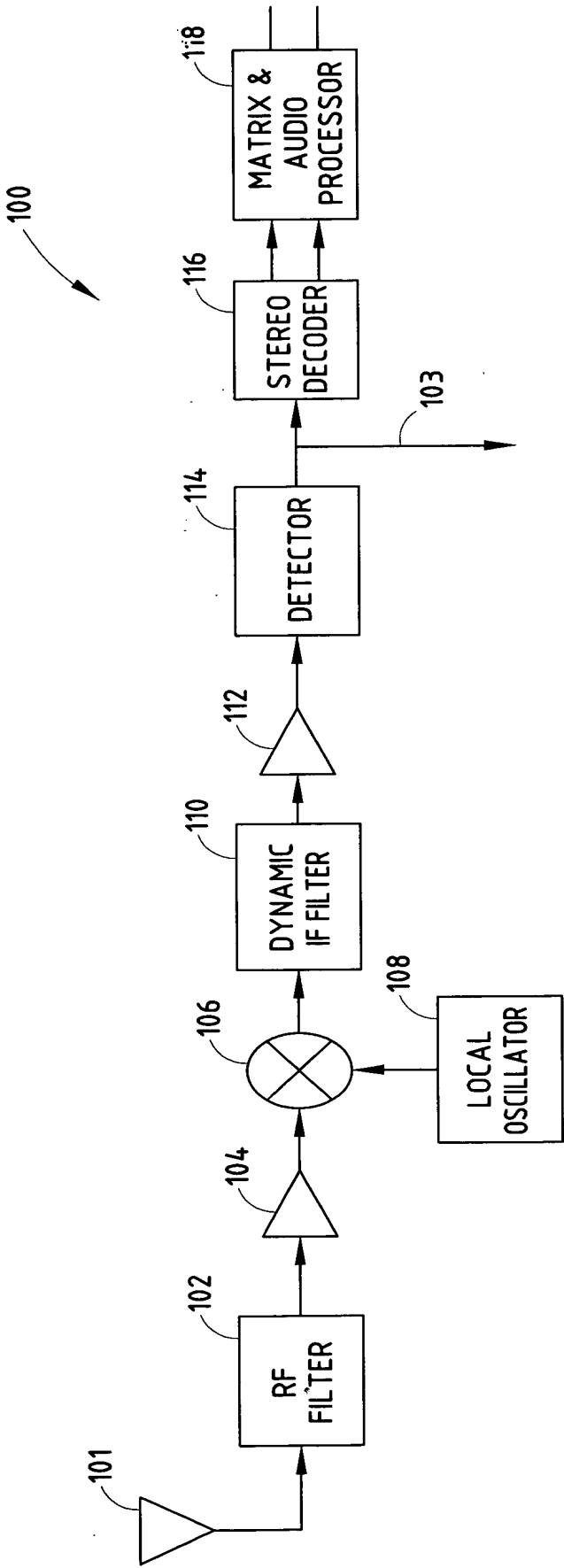


FIG. 1
PRIOR ART



FIG. 2

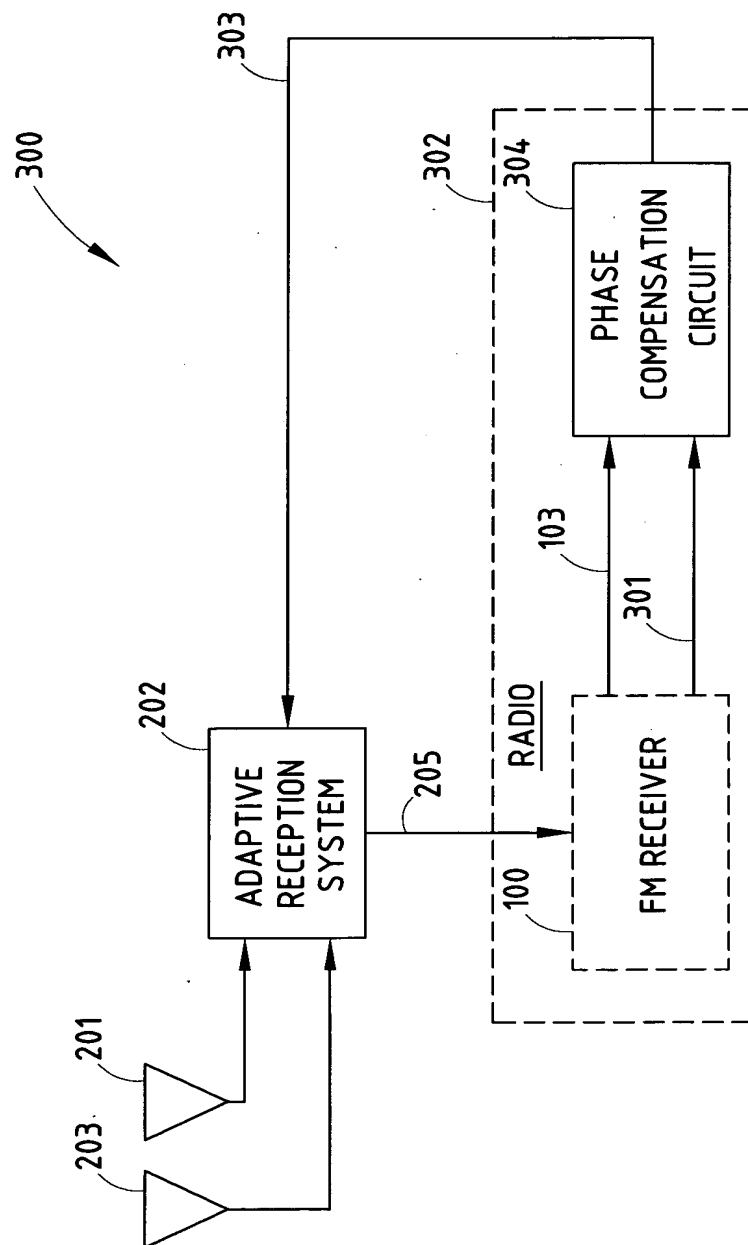


FIG. 3

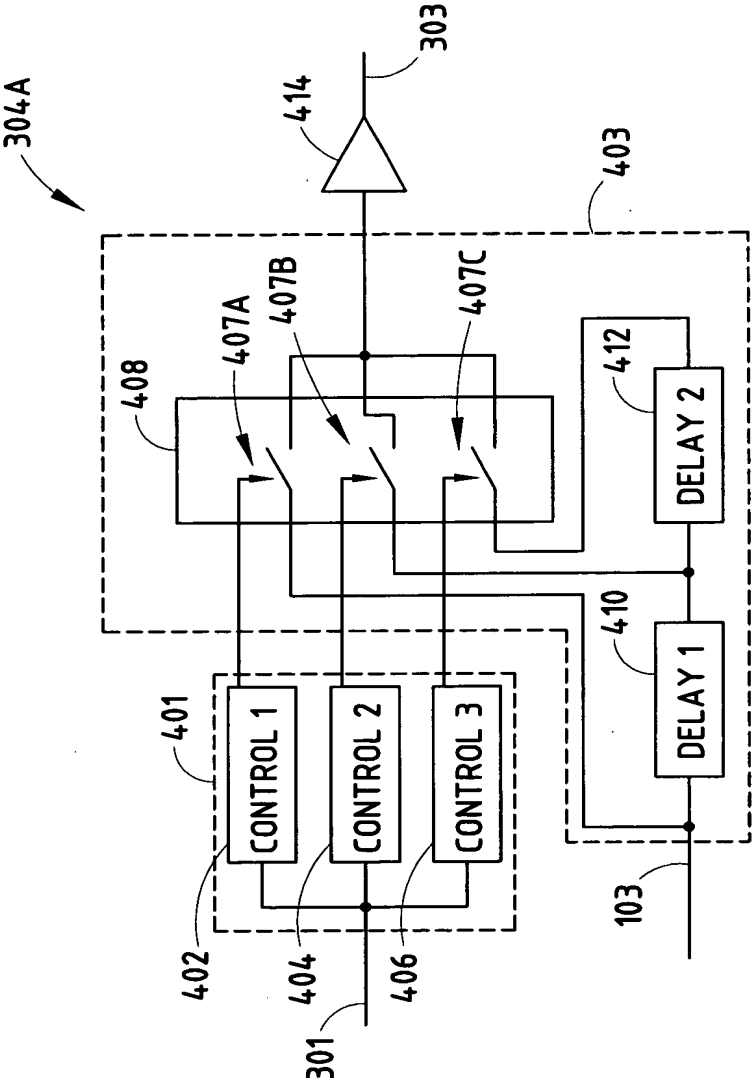


FIG. 4

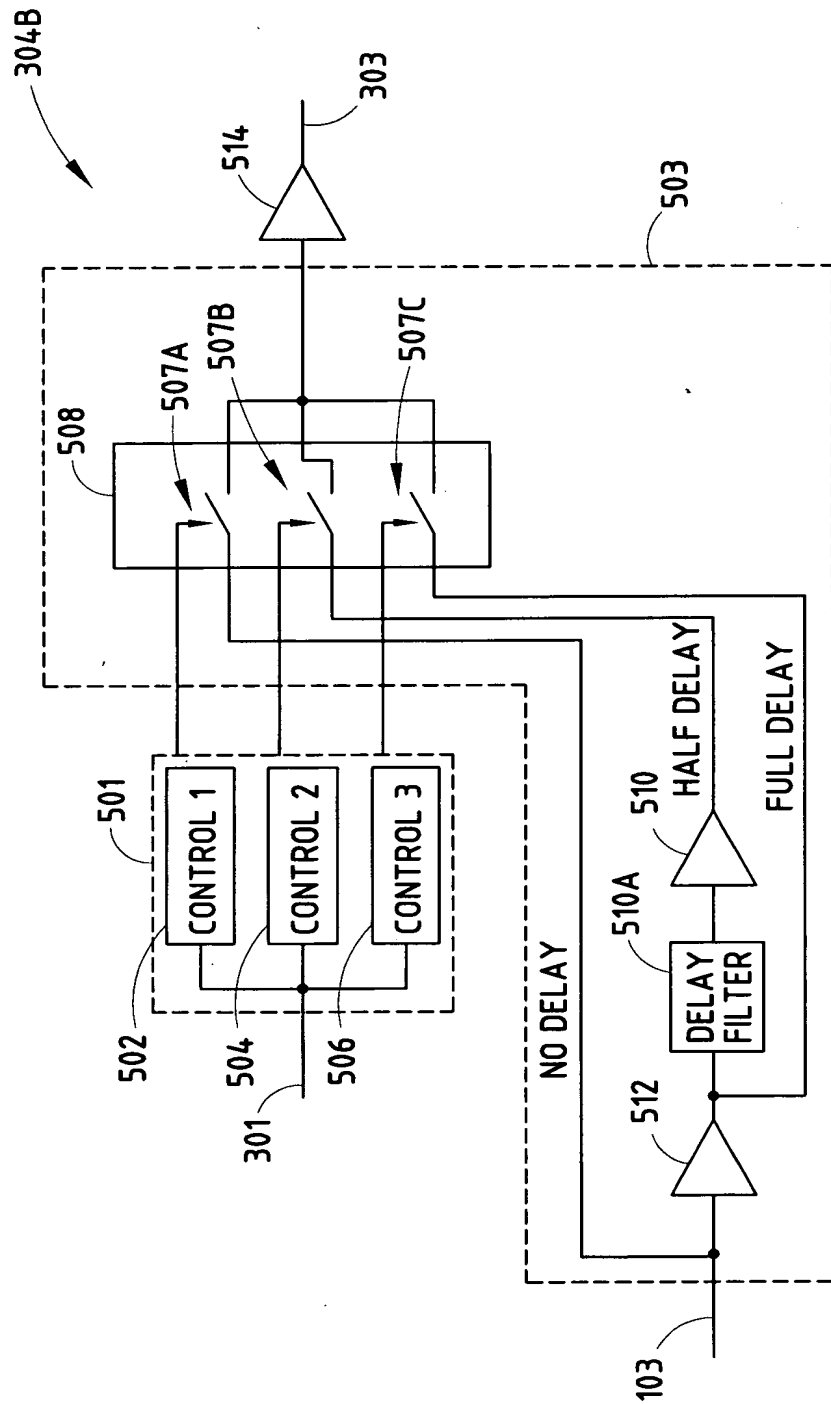


FIG. 5

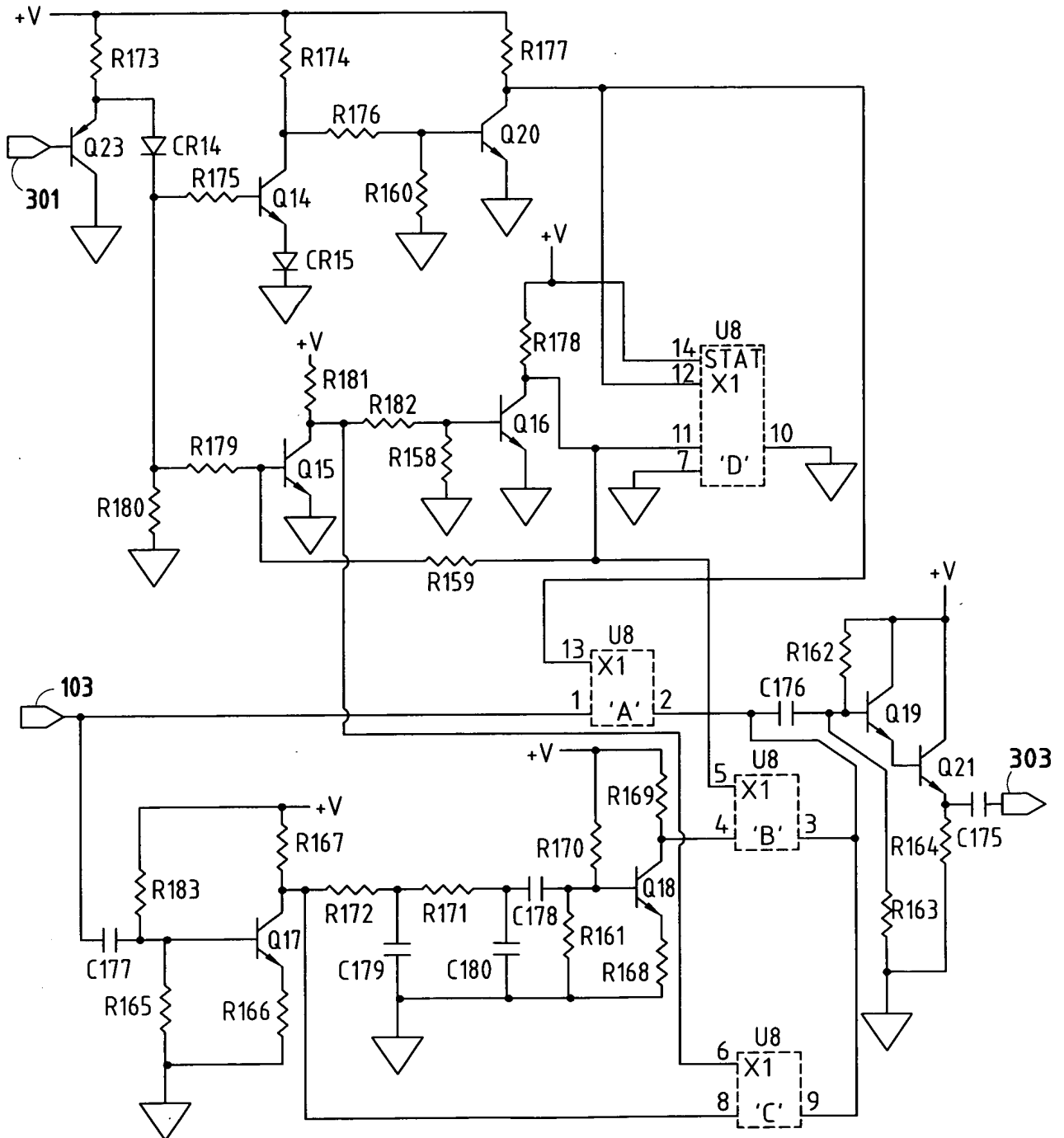


FIG. 6